

### **Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

### **Listing of Claims:**

1. (Currently Amended) A method of manufacturing a series of integrated circuits having related functionality, the method including the steps of:

(a) ~~—determining a unique 64 bit identifier for each integrated circuit in the series;~~

(b) ~~—permanently storing the identifiers in the respective~~ on one of the integrated circuits by selectively blowing 224 bits of fuses;

~~(c) —repeating steps (a) and (b) for each integrated circuit in the series;~~

(d) ~~—in each integrated circuit, mapping the identifier into a key K;~~

wherein the key of one of the integrated circuits is designated as a base key, and the key of at least one other integrated circuit is derived from the base key by applying a one-way function to the base key~~the identifiers for the series are determined in such a way that knowing the identifier of one of the integrated circuits does not improve the ability of an attacker to determine the identifier of any of the other integrated circuits.~~

2. (Original) A method according to claim 1, wherein the identifier for each integrated circuit is determined using a stochastic mechanism, thereby rendering highly improbable the replication of some or all of the series of identifiers stored on the series of the integrated circuits.

3. (Original) A series of integrated circuits having related functionality, wherein each of the integrated circuits incorporates an identifier determined and stored in accordance with claim 1.

4. (Previously Presented) A series of integrated circuits according to claim 3, wherein each of the integrated circuits is a printer controller.

5. (Original) A first integrated circuit of a series of integrated circuits according to claim 3, operable in first and second mode, wherein in the first mode, supervisor code can access the identifier and in the second mode, user code cannot access the identifier.

6. (Original) A first integrated circuit according to claim 5, wherein the supervisor mode is available to a program upon verification of that program by a boot program of the integrated circuit.

7 - 9. (Cancelled)

10. (Currently amended) A first integrated circuit according to claim 3~~5~~, configured to produce and output a message from the integrated circuit, the message including a result of encrypting key~~K~~.

11. (Currently amended) A method of injecting a key into a target integrated circuit, comprising the step of receiving the message generated by the first integrated circuit of claim 10, and transferring a second key into the target integrated circuit, the second key being based on ~~K~~the key.

12 - 20. (Cancelled)

21. (New) A method according to claim 1, wherein the key of the at least one other integrated circuit is derived from the base key by applying a one-way function to the base key and the identifier of the at least one other integrated circuit.